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Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DKT. NO. 5500-98000 APPLICANT: John V. Sell FILING DATE: April 12, 2004	SERIAL NO. 10/822,506 GROUP: 2173

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE APPROPRIAT
DM		5,949,423	9/7/99	Olsen			
		6,226,012	5/1/01	Priem, et al.			
		6,239,808	5/29/01	Kirk, et al.			
DM		6,266,064	7/24/01	Snyder			

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

DM	A1	Thomas Pabst, "High-Tech and Vertex Juggling - NVIDIA's New GeForce3 GPU," February 27. 2001, 16 pages.
	A2	Alekey Berillo, "S3TC and FXT1 Texture Compression," Copyright by Digit-Life.com, 1997-2003, 21 pages.
	A3	"Geode™ GX1 Processor Series Low Power Integrated x86 Solution," National Semiconductor Corporation, June 2002, 246 pages.
DM	A4	Ned Greene, Michael Kass, Gavin Miller, "Hierarchical Z-Buffer Visibility," Siggraph Conference Proceedings, August 1993.

EXAMINER: DM DATE CONSIDERED: 3/18/2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent own